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[348151G-AVR-07/10ATmega128Ato one enables the wait-state and one extra cycle is added during read/write strobe as shown inFigure 7-7.7.6.XMCRA - External Memory Control Register A· Bit 7 - Res: Reserved BitThis is a reserved bit and will always read as zero.](#)

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[Atmel Studio IDE \(Atmel-Studio \) Studio 7 is the integrated development platform \(IDP\) for developing and debugging all AVR@ and SAM microcontroller applications. The Atmel Studio 7 IDP gives you a seamless and easy-to-use environment to write, build and debug your applications written in C/C++ or assembly code.](#)

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[Data RD Memory access instruction @ @ AVR ATmega128A contains 4Kbytes of data EEPROM memory organized as a contains a detailed description on EEPROM programming is likely to rise or fall slowly on Power-up/down. This causes the device for some CC the EEPROM data can be corrupted because the supply voltage is CC, ... Page 22](#)

[ATMEGA128A-AU Atmel, ATMEGA128A-AU Datasheet](#)
[The low-power Atmel 8-bit AVR RISC-based microcontroller combines 8KB of programmable flash memory, 1KB of SRAM, 512B EEPROM, and a 6 or 8 channel 10-bit A/D converter. The device supports throughput of 16 MIPS at 16 MHz and operates between 2.7-5.5 volts.](#)

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[Most of us consider the hardware in the chip to be the "library" and the datasheet for the chip, the instructions on using the library. If you don't know my whole story, keep your mouth shut. If you know my whole story, you're an accomplice.](#)

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[8-Bit AVR Microcontroller with 16K Bytes In-System ProgrammableFlash, ATMEGA162 datasheet, ATMEGA162 circuit, ATMEGA162 data sheet : ATMEL, alldatasheet, datasheet, Datasheet search site for Electronic Components and Semiconductors, integrated circuits, diodes, triacs, and other semiconductors.](#)

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[ATMEGA128A-AU: LCSC Part # C6510: Package: TQFP-64_14x14x08P: Customer # Datasheet: Microchip Tech ATMEGA128A-AU. EasyEDA Libraries EasyEDA Model. Description: 8-Bit FLASH AVR 16MHz 2.7V - 5.5V TQFP-64_14x14x08P ATMEL & AVR RoHS](#)

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[What really confused me \(being new to AVR\) was that the ISP layout had SCK pin in it, but I couldn't find one while looking at the PDIP schematic. It wasn't until I looked at the MLF layout and noticed that SCK was there. I gave it a shot and hooked the ISP to the pin labeled SCL\(on the PDIP schematic\) and it worked.](#)

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